

UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

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First Named Inventor or Application Identifier:

Shuko SAITO et al.

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APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

ADDRESS TO: Assistant Commissioner for Patents
Box Patent Application
Washington, DC 20231

1. ☒ Fee Transmittal Form
2. ☒ Specification, Claims & Abstract [Total Pages: 22]
3. ☒ Drawing(s) (35 USC 113) [Total Sheets: 5]
4. ☒ Oath or Declaration [Total Pages: 4]
 - a. ☒ Newly executed (original or copy)
 - b. ☐ Copy from a prior application (37 CFR 1.63(d)) (for continuation/divisional with Box 17 completed)
 - i. ☐ **DELETION OF INVENTOR(S)**
Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).
5. ☐ Incorporation by Reference (usable if Box 4b is checked)
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
6. ☐ Microfiche Computer Program (Appendix)
7. ☐ Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)
 - a. ☐ Computer Readable Copy
 - b. ☐ Paper Copy (identical to computer copy)
 - c. ☐ Statement verifying identity of above copies

ACCOMPANYING APPLICATION PARTS

8. ☒ Assignment Papers (cover sheet & document(s))
9. ☐ 37 CFR 3.73(b) Statement (when there is an assignee) ☐ Power of Attorney
10. ☐ English Translation Document (if applicable)
11. ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS Citations
12. ☒ Preliminary Amendment
13. ☒ Return Receipt Postcard (MPEP 503) (Should be specifically itemized)
14. ☐ Small Entity Statement(s) ☐ Statement filed in prior application, status still proper and desired.
15. ☒ Certified Copy of Priority Document(s) (if foreign priority is claimed)
16. ☐ Other:

17. If a CONTINUING APPLICATION, check appropriate box and supply the requisite information:
☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No: 1
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Shuko SAITO et al.

Serial No.: Unassigned

Group Art Unit: Unassigned

Filed: December 1, 1999

Examiner: Unassigned

For: POWER SUPPLY CONTROL DEVICE AND INFORMATION PROCESSING
DEVICE USING SAME

Preliminary Amendment

*Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231*

Sir:

Before examination of the above-identified application, please amend the application as follows.

IN THE CLAIMS:

Please **AMEND** the claims as follows:

11. (AMENDED) The information processing device according to Claim 7 [or 8], wherein said switch unit includes

a first switch which activates or deactivates the power supply from said first power supply input terminal;

a second switch which activates or deactivates the power supply from said second power supply input terminal; and

a switch control unit which drives one switch and thereby activates the power supply from said first power supply input terminal when there is no power supply from said second power supply input terminal according to a result from said power supply input detection unit,

and which drives another switch and thereby activates the power supply from said second power supply input terminal when the power supplied by said second power supply input terminal is greater than the predetermined value.

Remarks

This Preliminary Amendment is submitted to improve the form of the specification as originally-filed.

It is respectfully requested that this Preliminary Amendment be entered in the above-referenced application.

Respectfully submitted,

STAAS & HALSEY LLP

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05 OCT 1999 15:43:10

TITLE OF THE INVENTION

POWER SUPPLY CONTROL DEVICE AND INFORMATION PROCESSING
DEVICE USING SAME

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention concerns a power supply control device to which power from a power supply is provided externally and an information processing device using the power supply control device.

2. Discussion of Related Art

Recent years have witnessed the development of increasingly miniature and highly portable information devices other than desktop and laptop computers, such as mobile computers, electronic notebooks, digital cameras and storage devices and their interconnection now allows the exchange of information.

In conjunction therewith, there is also an growing dissemination of interface specifications allowing power to be supplied, as represented by the USB (Universal Serial Bus), IEEE1394.

In the past, peripheral device units have been kept small to avoid compromising

portability and power has been supplied through an AC adapter or through a power supply terminal of a host personal computer interface connector.

However, the conventional method for supplying power has almost always been either an external supply using an AC adapter or the like, a battery or by way of an interface. That is because switching between two power supply inputs requires a switch for switching, or implementation of an "or" function using a diode, which have the following drawbacks, respectively.

Switching methods involving a switch have a cost disadvantage in that a switch must be installed and that power may be lost during switching. Further, if the mode of supplying power is interrupted, operation is not possible even if power can be otherwise supplied.

Methods using a diode are problematic in that, when the voltage on the interface side is high, switching does not actually occur even if an AC adapter is connected. In particular, IEEE1394 entails a relaxed specification of the power supply voltage. A broad specification with considerable latitude stipulating from 8V to 33V, or the lack thereof, as acceptable. This has required adoption of an AC adapter with a voltage higher than that maximum value and use of circuits with high withstand voltages, for example, and measures such as the provision of high-capacity capacitors.

FIG. 1 is a structural drawing of a conventional power supply control device in which 5V is supplied to an information processing unit. Power is supplied to this power supply control device from the power supply terminal of an interface connector

and from an AC adapter by way of respective signal lines 1, 2. The power supply voltage supplied from the interface connector is set in the range of 8V-33V, according to the personal computer, or the like, connected thereafter. The power supply voltage supplied from the AC adapter is greater than 34V.

5 Backflow prevention diodes 8, 9 are provided in the signal lines 1, 2, preventing current flowing from one line from flowing back into the other line. Thus, the power supply voltage is supplied to a power supply circuit 4 from the line with the higher voltage.

10 The power supply circuit 4 comprises a field effect transistor 19, which performs switching, a flywheel diode 24, a coil 25, output voltage feedback resistors 13, 14, a decoupling capacitor 21, a smoothing capacitor 23 and a control circuit 26.

15 The output voltage from the power supply circuit 4 is divided by resistors 13, 14. Based on the result of a comparison to a predetermined reference voltage, the control circuit 26 issues instructions for switching to the field effect transistor 19 and standardizing feedback control is thereby carried out to provide a 5V output voltage.

20 In such a power supply control device, when the power is supplied from the interface signal line 1, and not from the adapter signal line 2, current flows from the interface signal line 1 into the decoupling capacitor 21 and is prevented from flowing into the adapter signal line by a backflow prevention diode 9.

When the power supply voltage from the interface signal line 1 is 33V, even though power is supplied from the adapter signal line 2, if a higher power supply voltage is not supplied, current flows from the interface signal line 1 to the decoupling capacitor 21.

5 Essentially, when 8V-33V is supplied from the interface, a voltage higher than the 33V, that is the maximum voltage supplied from the interface, is required to ensure that when an AC adapter is used, no power is received from the interface and the current received comes only from the AC adapter. Thus, accounting for error in precision, a voltage greater than 34V is required.

10 Because the relationship between the withstand voltage and the capacity of a capacitor is generally proportional, the smoothing capacitor within the AC adapter must also be increased. The slim likelihood of increasing the conversion efficiency of the power supply for conversion from 34V to 5V is also a problem.

15 SUMMARY OF THE INVENTION

Consequently, an object of the present invention is to provide a power supply control device and an information processing device which reduces the wear of a host computer, or other such battery, connected by way of an interface connector. This object is achieved by receiving a prioritized power supply when power is provided by a
20 dedicated power supply source, which supplies power only from an AC adapter, or other such power supply.

Another object of the present invention is to provide a power supply control device and an information processing device which do not entail an unnecessary increase in the power supply voltage of an AC adapter, or other such power supply source, and to allow the use of a small, inexpensive power supply source.

5 Additional objects and advantages of the present invention will be set forth in part in the description which follows, and in part, will be obvious from the description, or may be learned by practice of the invention.

The power supply control device and information processing device using such possess an interface that receives or transmits information from or to another information processing device and is provided with a predetermined power supply. The aforementioned power supply control device is provided with a first power supply input terminal to which a power supply is provided by way of the aforementioned interface. Also provided is a second power supply input terminal to which a power supply is provided by way of another power supply source other than the aforementioned interface. A power supply input detection unit detects an instance when the input of the aforementioned second power supply input terminal is above a predetermined value. A switch unit cuts off the power supplied from the aforementioned first power supply input terminal and activates the power supply from the aforementioned second power supply input terminal when the input from the aforementioned second power supply input terminal is above a predetermined value, according to the aforementioned power supply input detection unit. A power supply

processor processes the power supplied by way of the first or second power supply input terminals. In addition, the first power supply input terminal is connected to an interface power supply and the second power supply input terminal is connected to an AC adapter.

5 The switch unit is provided with a switch which activates or deactivates the power supplied from the first power supply input terminal. A switch control unit drives a switch to cut off the power supplied from the first power supply input terminal when the power supplied from the second power supply input terminal is more than a predetermined value according to a result from the power supply input detection unit.

10 In addition, a backflow prevention unit is provided in the power supply lead from the first power supply input terminal and the second power supply input terminal, respectively.

15 The switch unit is also provided with a first switch, which activates or deactivates the power supplied from the first power supply input terminal and a second switch, which activates or deactivates the power supplied from the second power supply input terminal. Also provided is a switch control unit which drives one switch and thereby activates the power supply from the first power supply input terminal when there is no power supplied from the second power supply input terminal, according to a result from the power supply input detection unit, and drives another switch to activate a power supply from the second power supply input terminal when the power supplied is greater than a predetermined value from the second power supply input terminal. In

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addition, the switch comprises a field effect transistor and the aforementioned interface is a USB-specified or IEEE-specified interface.

The aforementioned information processing device of the present invention is a storage device which processes a disk, card, or tape form of a storage medium.

5 Accordingly, there is means which detects a high priority voltage from among two power supply inputs and means which cuts off the lower priority power supply input. Cutting off the lower priority power supply input by means of the output from the voltage detection means allows power to be automatically supplied from the higher priority power supply input terminal.

10 In addition, by using an AC adapter or other such power supply source whenever available and using an interface connector only when power is interrupted, the present invention provides an information processing device which does not unnecessarily wear the battery of a host computer.

15 In addition, without needlessly raising the voltage of an AC adapter, the present invention can also be formed compactly and inexpensively.

BRIEF DESCRIPTION OF THE DRAWINGS

20 These and other objects and advantages of the invention will become apparent and more readily appreciated from the following description of the preferred embodiments, taken in conjunction with the accompanying drawings of which:

FIG. 1 is a structural drawing of conventional power supply control device;

FIG. 2 is a structural drawing of the information processing device system of the present invention;

FIG. 3 is a structural drawing of a first preferred embodiment of the power supply control device of the present invention;

FIG. 4 is a structural drawing of a second preferred embodiment of the power supply control device of the present invention; and

FIG. 5 is a drawing depicting the input voltage of a power supply circuit and an efficiency curve.

DESCRIPTION OF THE PREFERRED EMBODIMENT

FIG. 2 is a structural drawing of an information processing device system. The information processing device 30 is supplied by, as an external power supply, an AC adapter 31 and by a power supply stored in the battery 34 of a host computer 33 powered by a battery 34 or an AC adapter 32 and provided by way of an interface by means of power supply leads 1, 2.

Power supply from the host computer 33 is accomplished by connection of the power supply lead of a detachable interface connector (I/F) to a first power supply input terminal at a device interface 39. Power supply from the AC adapter 31 is accomplished by connection of a detachable connector to a second power supply input terminal 35.

Though the present preferred embodiment describes an instance using an

IEEE1394-specified interface connector, items possessing a power supply lead or power supply terminal designated by IEEE specifications, USB specifications, or ISO specifications can also be used. A USB-specified interface provides a 5V power supply.

5 Voltage supplied by way of the first and second power supply input terminals 39, 35, respectively, is supplied to a power supply processor 37 in the power supply control device 40. Provision of a power supply input detector 36 to detect when the input from the second power supply input terminal 35 is above a predetermined value, affords a structure switched by a switching unit 37a such that supplied power is received with priority from the AC adapter 31 when a predetermined power supply from the AC adapter 31 is present.

10 The power supply processor 37 outputs a predetermined 5V power supply voltage, which is supplied to an information processing unit 38. The information processing unit 38, that is the device to which power is ultimately supplied, may be an opto-magnetic disk device, which is both an optical storage device and a computer storage device, a phase change optical disk device, a DVD device, a CD-ROM/CD-R/CD-RW device, an optical card device, a floppy disk device, a hard disk device, a magnetic disk device, a magnetic card device, a magnetic tape device or an optical tape device, or the like. A mobile phone or a portable and compact terminal such as a
15 notebook personal computer, pocket personal computer, electronic notebook or
20 electronic book may also be used.

Figure 3 is a structural drawing of a first preferred embodiment of the power supply control device of the present invention. Voltage supplied by an AC adapter 31 is detected by a voltage detection unit 5 comprising a power supply input detection unit. Voltage detection unit 5 comprises a Zener diode 10, resistors 11, 12; and transistors 17, 18. A switch unit 6 comprises resistors 15, 16 and a field effect transistor 20. Backflow prevention diodes 8, 9 are provided in the signal lines 1, 2, respectively and prevent current flowing from one direction from flowing backwards in the other direction.

A power supply circuit 4 comprises a field effect transistor 19 which performs switching, a flywheel diode 24, a coil 25, output voltage feedback resistors 13, 14, a decoupling capacitor 21, a smoothing capacitor 23 and a control circuit 26.

The output voltage from the power supply circuit 4 is divided by resistors 13, 14 based on the result of a comparison to a predetermined reference voltage. The control circuit 26 issues instructions for switching to the field effect transistor 19 and standardizing feedback control is thereby carried out to provide a 5V output voltage.

In the voltage detection unit 5, when the Zener diode 10 detects that the flow of current supplied from the AC adapter 31 and input by way of the second power supply input terminal 35 has a voltage exceeding 8V, a transistor 17 switches ON. When this transistor 17 switches ON, another transistor 18 switches OFF. When that transistor 18 switches OFF, the gate voltage of the field effect transistor 20 in the switch unit 6 is no

longer applied and the voltage supplied from the interface and input by way of the first power supply input terminal 39 is cut off. Here, the transistors 17, 18 and resistors 15, 16 make up a switch control unit that operates as a switch.

Thus, the only supply to the power supply circuit 4 is the input from the AC adapter 31. Conversely, if the supply from the AC adapter 31 is interrupted and the voltage falls below 8V, a transistor 17 switches OFF instantly, another transistor 18 switches ON, the gate voltage of the field effect transistor 20 falls below the source voltage and conducting status is achieved. Then, switchover occurs to supply power from the interface input by way of the first power supply input terminal 39. Thus, the power supply source can be made switchable simply by setting the AC adapter to greater than 8V.

Figure 4 is a structural drawing of a second preferred embodiment of the power supply control device pertaining to the present invention. Voltage supplied by an AC adapter 31 is detected by a voltage detection unit 5' comprising a power supply input detection unit. Voltage detection unit 5' comprises a Zener diode 10, resistors 11, 12, and transistors 17, 18. A switch unit comprises a switch control unit 7 and a switch located within a power supply circuit 4'.

The switch control unit 7 includes a NOT circuit 27 and NAND circuits 28, 29. The power supply circuit 4' comprises field effect transistors 19, 20 which include switches, a flywheel diode 24, a coil 25, output voltage feedback resistors 13, 14, a decoupling capacitor 21, a smoothing capacitor 23 and a control circuit 26.

The output voltage from the power supply circuit 4' is divided by resistors 13, 14 and a comparison to a predetermined reference voltage is performed. Based on that result, the control circuit 26 outputs a signal to the NAND circuits 28, 29, and standardizing feedback control is carried out to provide a 5V output voltage by instructing switching of the field effect transistors 19, 20.

In the voltage detection unit 5', when the Zener diode 10 detects that the flow of current supplied from the AC adapter 31 and input by way of the second power supply input terminal 35 has a voltage exceeding 8V, a transistor 17 switches ON. When this transistor 17 switches ON, a signal is input to the NOT circuit 27 and a NAND circuit 29 in the switch control unit 7, a field effect transistor 20 comprising a switch falls below the source voltage and conducting status is achieved. Thus, power is supplied from the AC adapter 31 and input by way of the second power supply input terminal 35. Hence, the only supply to the power supply circuit 4 is the input from the AC adapter 31.

Conversely, if the supply from the AC adapter 31 is interrupted and the voltage falls below 8V, a transistor 17 switches OFF instantly, a signal is input to a NAND circuit 28, the gate voltage of the field effect transistor 19 comprising a switch falls below the source voltage and conducting status is achieved. Thus, switchover occurs to supply power from the interface input by way of the first power supply input terminal 39. Hence, the power supply source can be made switchable simply by setting the AC adapter 31 to greater than 8V.

Essentially, in the present preferred embodiment, by duplicating the power supply circuit switching elements themselves and exercising selective control through a voltage detection unit signal, backflow prevention diodes are eliminated and the loss resulting from their voltage drop (approximately 0.7V) is reduced.

5 This 0.7V is the junction voltage of a junction-type diode and is generally on the order of 0.5V to 1V, but for the most part approximates 0.7V. This results in a voltage drop during current flow in the forward direction of the diode, leading to a corresponding loss of power.

Figure 5 is a drawing depicting an input voltage-efficiency curve which indicates switching efficiency with respect to the input voltage of the power supply input circuit. In Figure 5, the highest efficiency is at 12V-15V and the use of the above-described power supply control device affords selection of the AC adapter. This results in a highly efficient voltage. Precision is also not required in AC adapter output at this level of voltage, which allows the use of a very inexpensive AC adapter.

Efficiency is also poor at 34V and, thus, in lieu of using an expensive AC adapter in the above-discussed conventional power supply control device, the present invention solves the problem of poor power supply efficiency.

Though the foregoing preferred embodiment discussed power supplied from an AC adapter and an interface, application is also possible in an instance such as prioritized receipt from one of two interfaces supplying power. In addition, other than an AC adapter, a battery provided in an information processing device is also an

acceptable power supply source.

Detection of power supply input from a power supply source subject to prioritized receipt and performance of power supply switching is also acceptable. Selection among a plurality of more than two power supply sources may also be afforded. Thus, the present invention allows assignment of priority with respect to multiple power supply inputs and can afford automatic selection without impairing the degree of freedom of voltage in those power supply inputs.

"Degree of freedom of voltage" refers to the ability to design a voltage best suited to considerations such as energy efficiency, economics and mass production effects, without limitations placed on voltage design values by secondary restrictions.

The power supply control device can also be formed on a printed circuit board and a first and second power supply input terminal can be provided on the printed circuit board. A design can also be created entailing connection, either directly between connectors or by way of an FPC, from the printed circuit board to a power supply circuit on the control circuit board of a storage device or other such information processing unit.

Additionally, though in the foregoing preferred embodiment the power supply voltage supplied to an information processing unit was set at 5V and the detection voltage was set at greater than 8V, it is naturally the case that the power supply voltage supplied to an information processing unit can be modified to suit that specific unit. Specifically, the power supply voltage supplied to a power supply processing unit can

be set at 3.5V and the detection voltage can be set at 5V.

As described above, the power supply control device and information processing device pertaining to the present invention, through a simple structure, allows the receipt of power from a high priority power supply input among a plurality of power supply inputs. Thus, a power supply from a dedicated power supply source such as an AC adapter can automatically be received in a prioritized fashion, thereby affording a reduction in the wear of an interface-connected host, or the like, from a power supply.

By connecting a power supply such as an AC adapter, a user obviates the need to receive power from another information processing device through an interface, which is particularly beneficial in the case of a portable battery-driven notebook personal computer, or the like where it is desirable that the connection reduce battery wear as much as possible.

In addition, the power supply control device and information processing device pertaining to the present invention do not require the use of a large, high-precision and expensive AC adapter, allowing the use of a small and inexpensive AC adapter.

WHAT IS CLAIMED IS:

1. A power supply control device comprising:

first and second power supply input terminals

a power supply input detection unit determining whether an input of said second power supply input terminal is above a predetermined value;

5 a switching unit cutting off power supplied by said first power supply input terminal and activating a power supplied by said second power supply input terminal if the input to said second power supply input terminal is above the predetermined value; and

a power supply processor processing the power supplied by one of said first or second power supply input terminals.

2. The power supply control device according to claim 1, wherein said first power supply input terminal is connected to an interface power supply and said second power supply input terminal is connected to an AC adapter.

3. The power supply control device according to claim 1, wherein said switching unit includes

a switch activating or deactivating the power supplied by said first power supply input terminal, and

5 a switch control unit driving the switch thereby cutting off the power supplied

by said first power supply input terminal if the power supplied by said second terminal is greater than the predetermined value.

4. The power supply control device according to claim 3, wherein a backflow prevention unit is disposed in a power supply lead from said first power supply input terminal and a power supply lead from said second power supply input terminal.

5. The power supply control device according to claim 1, wherein said switching unit includes

a first switch activating or deactivating the power supplied by said first power supply input terminal;

a second switch which activating or deactivating the power supplied by said second power supply input terminal; and

a switch control unit driving one of the first and second switches, thereby activating the power supplied by said first power supply input terminal when there is no power supplied by said second power supply input terminal according to a result from said power supply input detection unit, and driving the other of the first and second switches, thereby activating the power supplied by said second power supply input terminal when the power supplied by said second power supply input terminal is greater than the predetermined value.

6. The power supply control device according to Claim 3, wherein the switch includes a field effect transistor.

7. An information processing device having an interface which receives or transmits information to and from another information processing device and a power supply control device to which a predetermined power supply is provided, comprising:

a first power supply input terminal to which a power supply is provided via said interface;

a second power supply input terminal to which a power supply is provided via another power supply source other than the interface;

a power supply input detection unit which detects an instance when the input of said second power supply input terminal is above a predetermined value;

a switch unit which cuts off a power supply from said first power supply input terminal and activates a power supply from said second power supply input terminal when the input from said second power supply input terminal is above the predetermined value according to said power supply input detection unit; and

a power supply processor which, for the predetermined power supply, processes the power supply supplied via said first or second power supply input terminals.

8. The information processing device according to Claim 7, wherein said

first power supply input terminal is connected to an interface power supply and said second power supply input terminal is connected to an AC adapter.

9. The information processing device according to claim 7, wherein said switch unit includes

a switch which activates or deactivates a power supply from said first power supply input terminal, and

a switch control unit which drives the switch and thereby cuts off a power supply from said first power supply input terminal when there is a supply of more than the predetermined value from said second power supply input terminal according to a result from said power supply input detection unit.

10. The information processing device according to Claim 9, wherein a backflow prevention unit is disposed in a power supply lead from said first power supply input terminal and said second power supply input terminal, respectively.

11. The information processing device according to Claim 7 or 8, wherein said switch unit includes

a first switch which activates or deactivates the power supply from said first power supply input terminal;

a second switch which activates or deactivates the power supply from said

second power supply input terminal; and

a switch control unit which drives one switch and thereby activates the power supply from said first power supply input terminal when there is no power supply from said second power supply input terminal according to a result from said power supply input detection unit, and which drives another switch and thereby activates the power supply from said second power supply input terminal when the power supplied by said second power supply input terminal is greater than the predetermined value.

12. The information processing device according to claim 9, wherein said switch comprises a field effect transistor.

13. The information processing device according to claim 7, wherein the interface is a USB-specified or an IEEE-specified interface.

14. The information processing device according to claim 7, wherein the information processing device is a storage device which processes a disk form, card form or tape form of a storage medium.

15. A power supply control device comprising:
first and second input terminals;

a detection unit determining whether an input of said second power supply input terminal is above a predetermined value; and

5

a switching unit cutting off power supplied by said first input terminal and activating a power supplied by said second input terminal if the input to said second input terminal is above the predetermined value.

16. A method of controlling a power supply control device comprising:

determining whether an input of a second power supply input terminal is above a predetermined value; and

switching off power supplied by a first input terminal and activating a power supplied by the second input terminal if the input to the second input terminal is above the predetermined value.

ABSTRACT OF THE DISCLOSURE

A power supply control device is provided with a first power supply input terminal to which a power supply is provided and a second power supply input terminal to which a power supply is provided. A power supply input detection unit detects an instance when the input of the second power supply input terminal is above a predetermined value and then a switch unit cuts off power supplied from the first power supply input terminal and activates power supply from the second power supply input terminal when the input from the second power supply input terminal is above a predetermined value according to the power supply input detection unit. A power supply processor processes a power supply supplied by way of the first or second power supply input terminal.

Nov. 22 '99
S.S.
A.M.
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S.F.

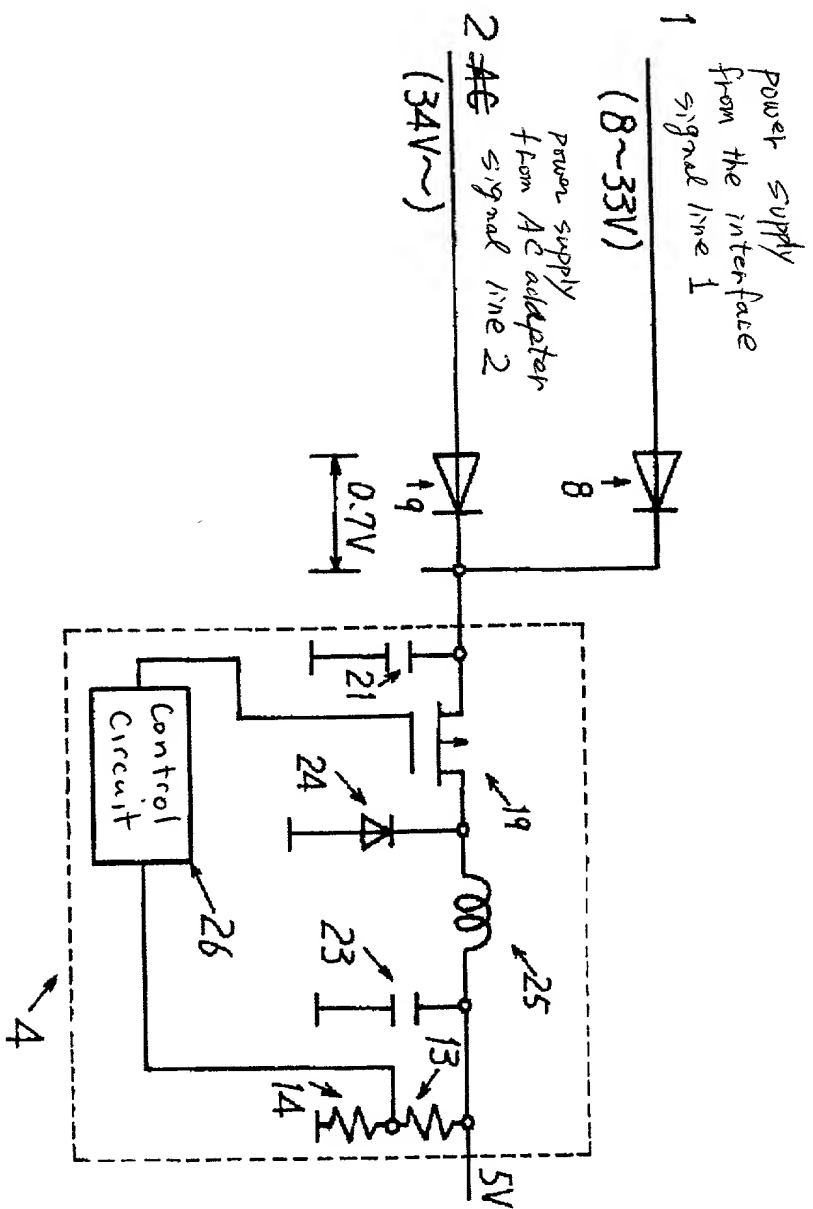


FIG. 1

PRIOR ART

09452151.420199

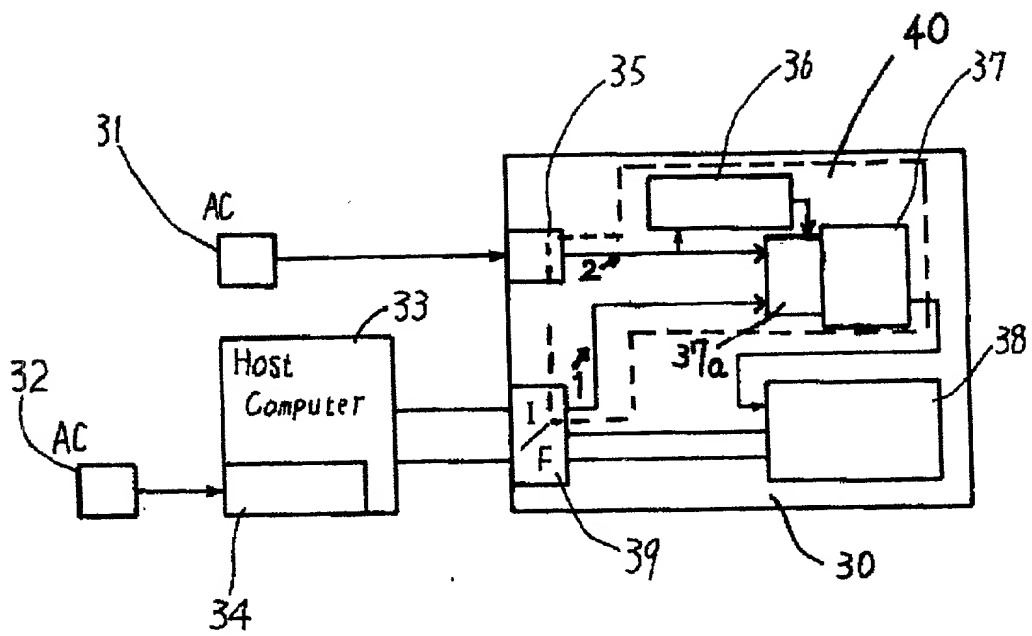
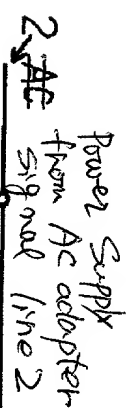


FIG. 2

5 5 4 5
5 5 5 5

(8~33V)



716.4

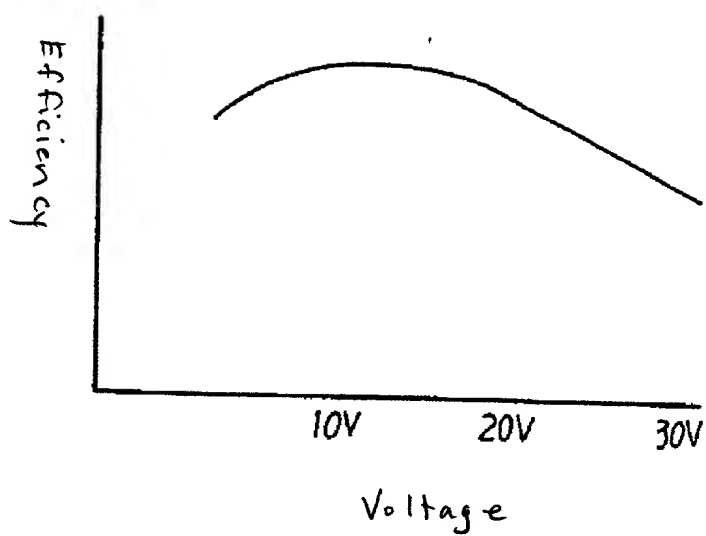


FIG. 5

Declaration and Power of Attorney For Patent Application

特許出願宣言書及び委任状

Japanese Language Declaration

日本語宣言書

下記の氏名の発明者として、私は以下の通り宣言します。

As a below named inventor, I hereby declare that:

私の住所、私書箱、国籍は下記の私の氏名の後に記載された通りです。

My residence, post office address and citizenship are as stated next to my name.

下記の名称の発明に関して請求範囲に記載され、特許出願している発明内容について、私が最初かつ唯一の発明者（下記の氏名が一つの場合）もしくは最初かつ共同発明者であると（下記の名称が複数の場合）信じています。

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

POWER SUPPLY CONTROL DEVICE AND

INFORMATION PROCESSING DEVICE USING SAME

上記発明の明細書（下記の欄でx印がついていない場合は、本書に添付）は、

the specification of which is attached hereto unless the following box is checked:

☐ 月 日に提出され、米国出願番号または特許協定条約国際出願番号を _____ とし、
(該当する場合) _____ に訂正されました。

☐ was filed on _____
as United States Application Number or
PCT International Application Number
_____ and was amended on
_____ (if applicable).

私は、特許請求範囲を含む上記訂正後の明細書を検討し、内容を理解していることをここに表明します。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

私は、連邦規則法典第37編第1条56項に定義されたとおり、特許資格の有無について重要な情報を開示する義務があることを認めます。

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Japanese Language Declaration
(日本語宣言書)

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Prior Foreign Application(s)

外国での先行出願

11-092500

(Number)
(番号)

Japan

(Country)
(国名)

31, March 1999

(Day/Month/Year Filed)
(出願年月日)

Priority Not Claimed

優先権主張なし

☐(Number)
(番号)(Country)
(国名)(Day/Month/Year Filed)
(出願年月日)☐

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(Application No.)
(出願番号)(Filing Date)
(出願日)(Application No.)
(出願番号)(Filing Date)
(出願日)

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(Application No.)
(出願番号)(Filing Date)
(出願日)(Status: Patented, Pending, Abandoned)
(現況: 特許許可済、係属中、放棄済)(Application No.)
(出願番号)(Filing Date)
(出願日)(Status: Patented, Pending, Abandoned)
(現況: 特許許可済、係属中、放棄済)

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I hereby claim the benefit under Title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below.

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code Section 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of application.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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Japanese Language Declaration (日本語宣言書)

委任状 私は下記の発明者として、本出願に関する一切の手続きを米特許商標局に対して遂行する弁理士または代理人として、下記の者を指名いたします。(弁護士、または代理人の氏名及び登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith (list name and registration number)

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